

### AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application.

Claims 1-31 (Cancelled)

32. (Currently Amended) A computer comprising:

a base station having [[a processor and]] a Random Access Memory; and

a computing display subsystem detachably connectable to the base station, the computing display subsystem including a processor, a display controller, a storage device, and a communication adapter to communicate with the base station when the computing display subsystem is detached from the base station.

33. (Previously Presented) The computer of claim 32, wherein the computing display subsystem comprises a non-volatile storage device.

34. (Previously Presented) The computer of claim 33, wherein the non-volatile storage device comprises a flash memory.

35. (Currently Amended) The computer of claim 32, wherein the processor of the computing display subsystem is operable to operate at a higher frequency power mode [[when connected to the base station]] and at a lower frequency power mode.

36. (Previously Presented) The computer of claim 35, wherein the processor of the computing display subsystem is operable to operate using Intel® SpeedStep™ Technology.

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37. (Previously Presented) The computer of claim 32, wherein the computing display subsystem includes an I/O controller of the computing display subsystem to receive data entered via a writeable liquid crystal display.

38. (Currently Amended) The computer of claim 32, wherein said base station includes a processor, a keyboard and a connection to a network.

39. (Currently Amended) A method comprising:

a base station transmitting data to a display subsystem, the base station having [[a processor and]] a Random Access Memory; and

the display subsystem receiving the data from the base station, the display subsystem detachably connectable to the base station, the display subsystem including a processor, a display controller, a storage device, and a communication adapter to communicate with the base station when the computing display subsystem is detached from the base station.

40. (Previously Presented) The method of claim 39, further comprising the display subsystem storing the data in a non-volatile storage device of the display subsystem.

41. (Currently Amended) The method of claim 39, further comprising the processor of the display subsystem operating at a higher frequency power mode [[when connected to the base station]] and operating at a lower frequency power mode [[when detached from the base station]].

42. (Previously Presented) The method of claim 39, further comprising writing on a writeable liquid crystal display of the display subsystem when the display subsystem is detached from the base station.

43. (Previously Presented) A computing display subsystem comprising:

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a processor;

a display controller;

a storage device;

a communication adapter to communicate with a base station when the computing display subsystem is detached from the base station; and

detachable connection to the base station.

44. (Previously Presented) The computing display subsystem of claim 43, wherein the storage device comprises a non-volatile storage device.

45. (Previously Presented) The computing display subsystem of claim 44, wherein the non-volatile storage device comprises a flash memory.

46. (Currently Amended) The computing display subsystem of claim 43, wherein the processor is operable to operate at a higher frequency power mode [[when the computing display subsystem is connected to the base station]] and at a lower frequency power mode [[when the computing display subsystem is detached from the base station]].

47. (Previously Presented) The computing display subsystem of claim 46, wherein the processor is operable to use Intel® SpeedStep™ Technology.

48. (Previously Presented) The computing display subsystem of claim 43, wherein the computing display subsystem includes a power supply separate from the base station, and wherein the communication adapter is operable to communicate with the base station using one or more of infrared signals and radio frequency signals.

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49. (Previously Presented) The computing display subsystem of claim 43, further comprising an I/O controller of the computing display subsystem to receive data entered via a writeable liquid crystal display.

50. (Previously Presented) A computing display subsystem comprising:

a processor;

a display controller;

a storage device;

a communication adapter to communicate with a base station when the computing display subsystem is detached from the base station; and

a connector to allow the computing display subsystem to be connected and detached from the base station,

wherein the processor is operable to operate at a higher frequency power mode when the computing display subsystem is connected to the base station, and at a lower frequency power mode when the computing display subsystem is detached from the base station.

51. (Previously Presented) The computing display subsystem of claim 50, wherein the storage device comprises a non-volatile storage device.

52. (Previously Presented) The computing display subsystem of claim 51, wherein the non-volatile storage device comprises a flash memory.

53. (Previously Presented) The computing display subsystem of claim 50, wherein the processor is operable to use Intel® SpeedStep™ Technology.

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54. (Previously Presented) The computing display subsystem of claim 50, further comprising an I/O controller of the display subsystem to receive data entered via a writeable liquid crystal display.

55. (New) The computer of claim 32, wherein the processor has a plurality of power consuming modes.

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